

FIG. 1A

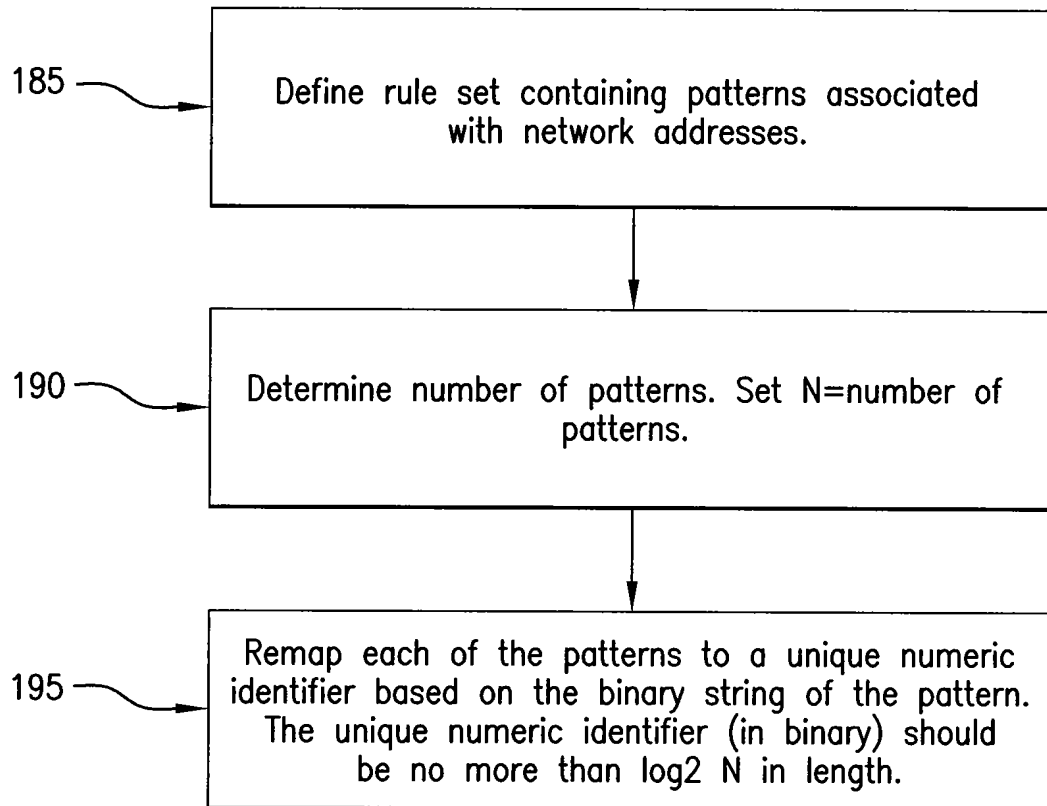
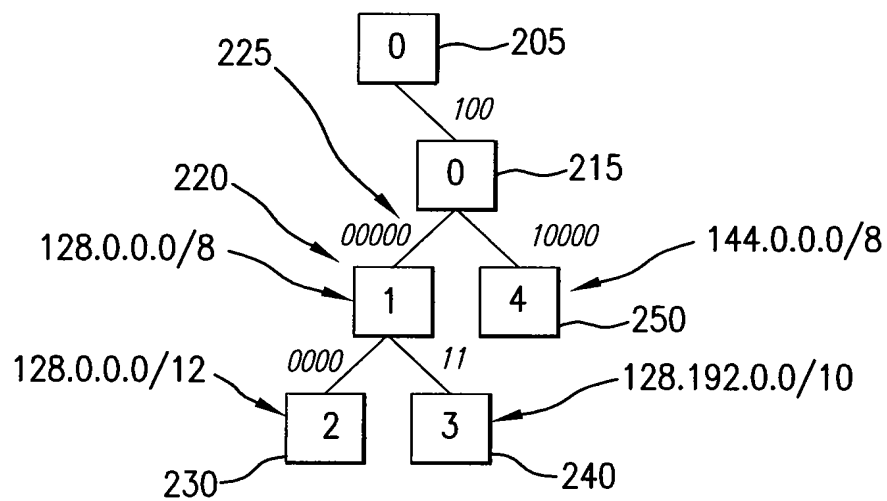
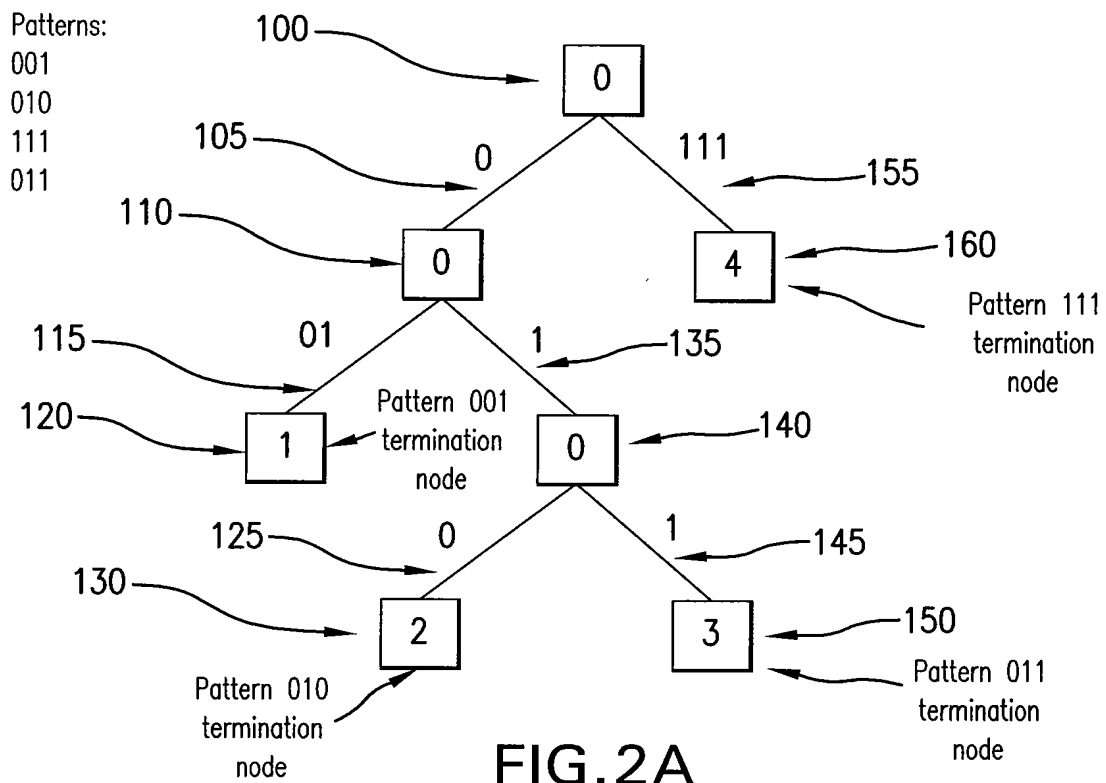


FIG. 1B



300

Pattern	Lower	Upper
128.0.0.0/8	1	3
144.0.0.0/8	4	4
128.0.0.0/12	2	2
128.192.0.0/10	3	3

310  
320  
330  
340

FIG. 3

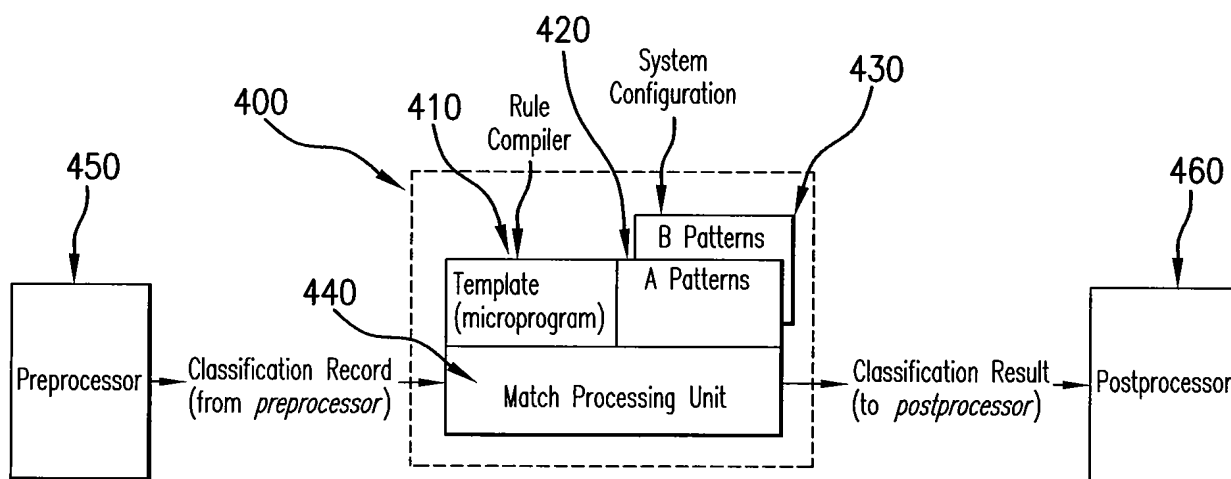


FIG. 4

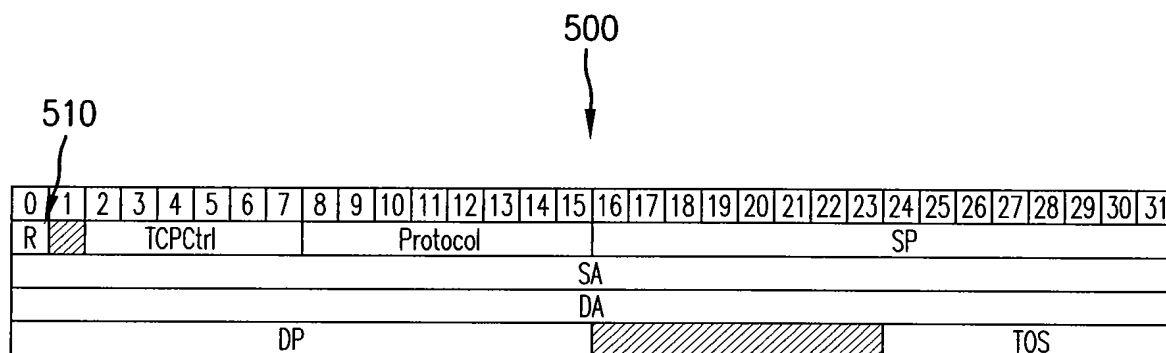
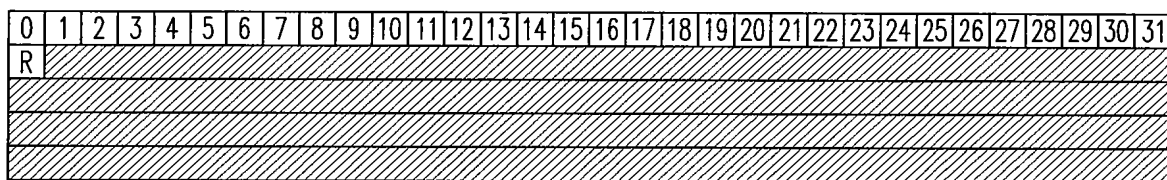


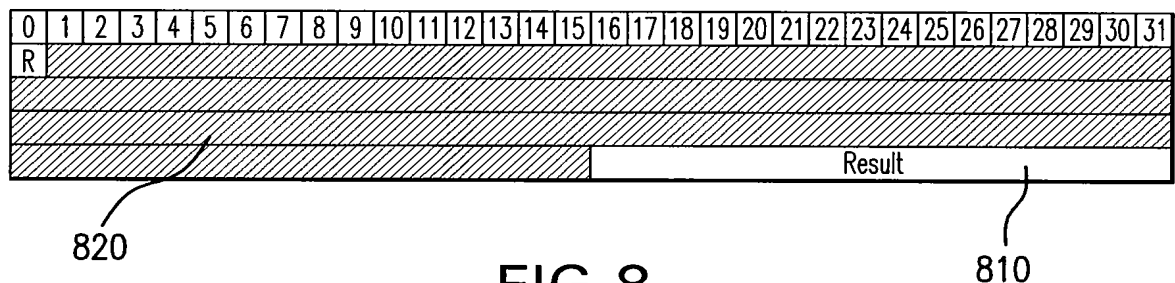
FIG. 5



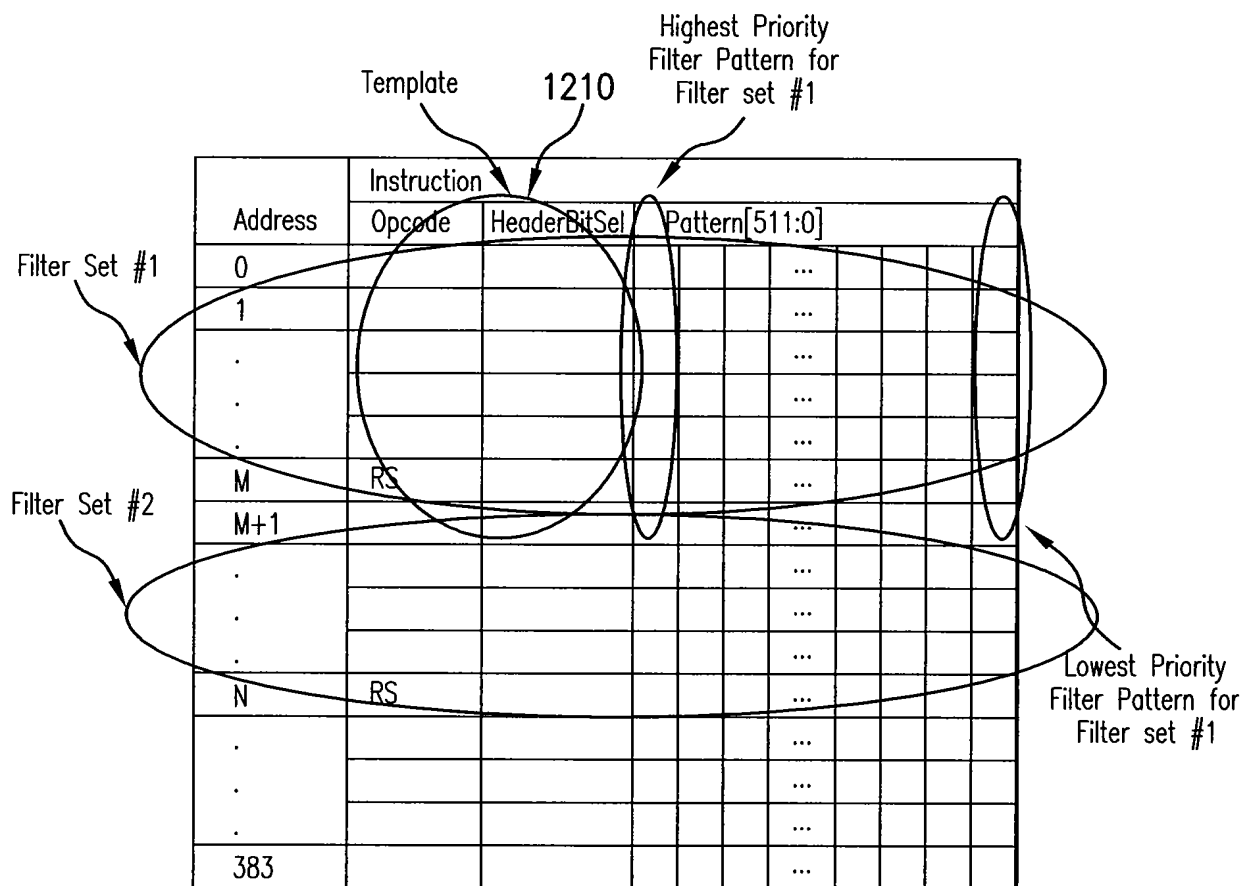
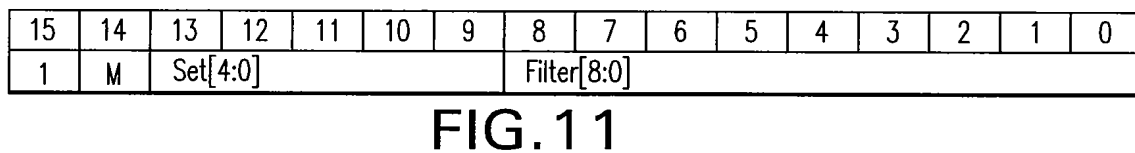
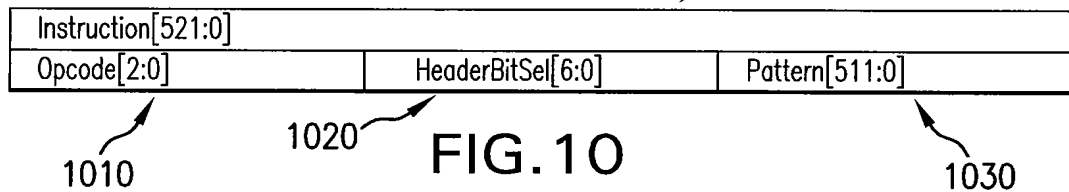
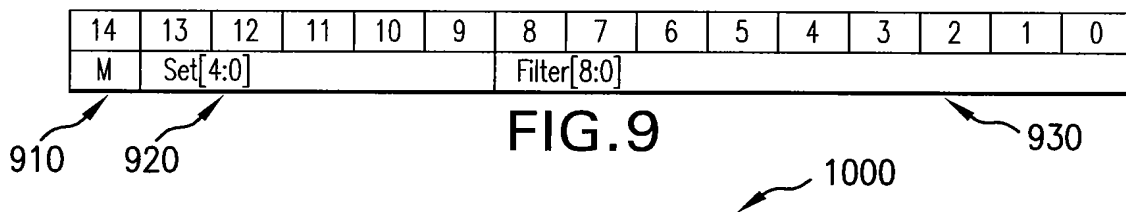
**FIG. 6**

Field	Layer	Operation	#Header Bits	#Pattern Bits	Notes
SA	3	Mask/Data	32	64	Each header bit requires a pattern mask and pattern data bit.
DA		Mask/Data	32	64	
Protocol		FieldMask/Data	8	9	The mask for these fields may be applied to all bits in the field simultaneously.
TOS		FieldMask/Data	8	9	
SP	4	Range	16	32	Since a range requires a low limit and a high limit, the pattern bits must specify these limits.
DP		Range	16	32	
TCPCtrl		Flag	6	6	A flag only requires a single pattern bit that indicates either the value of flag is a don't care or it must be set for the rule to match.
Total			120	216	

**FIG. 7**



**FIG. 8**



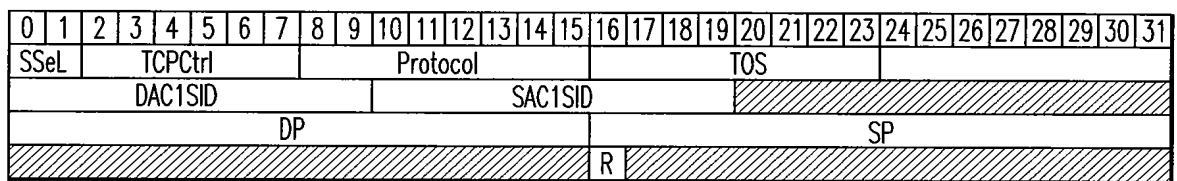


FIG.13

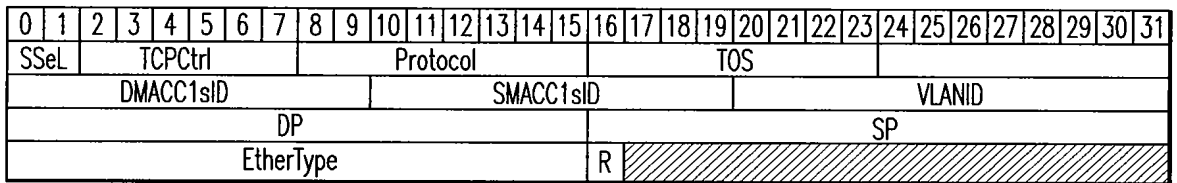


FIG.14

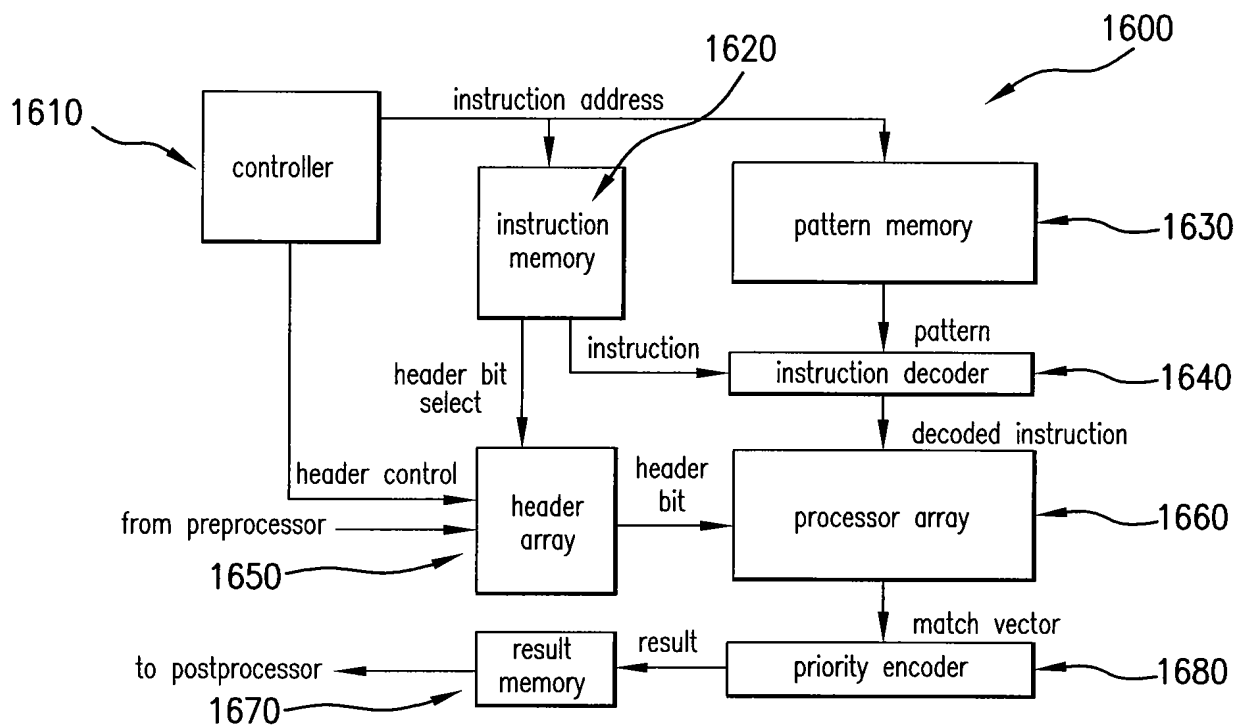


FIG. 15

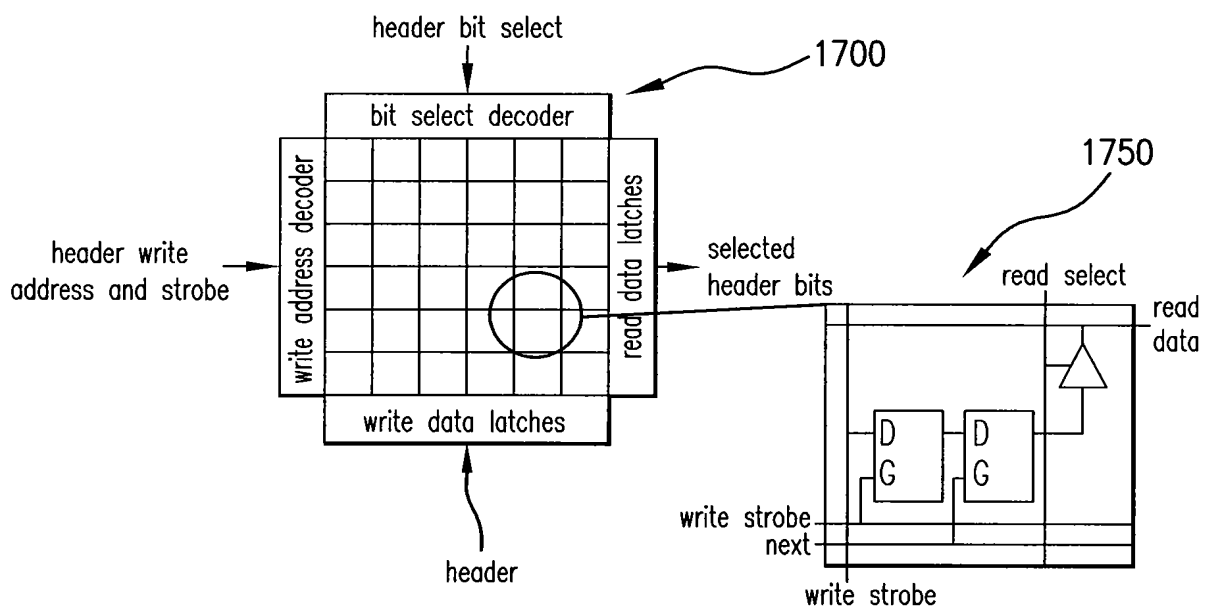


FIG. 16



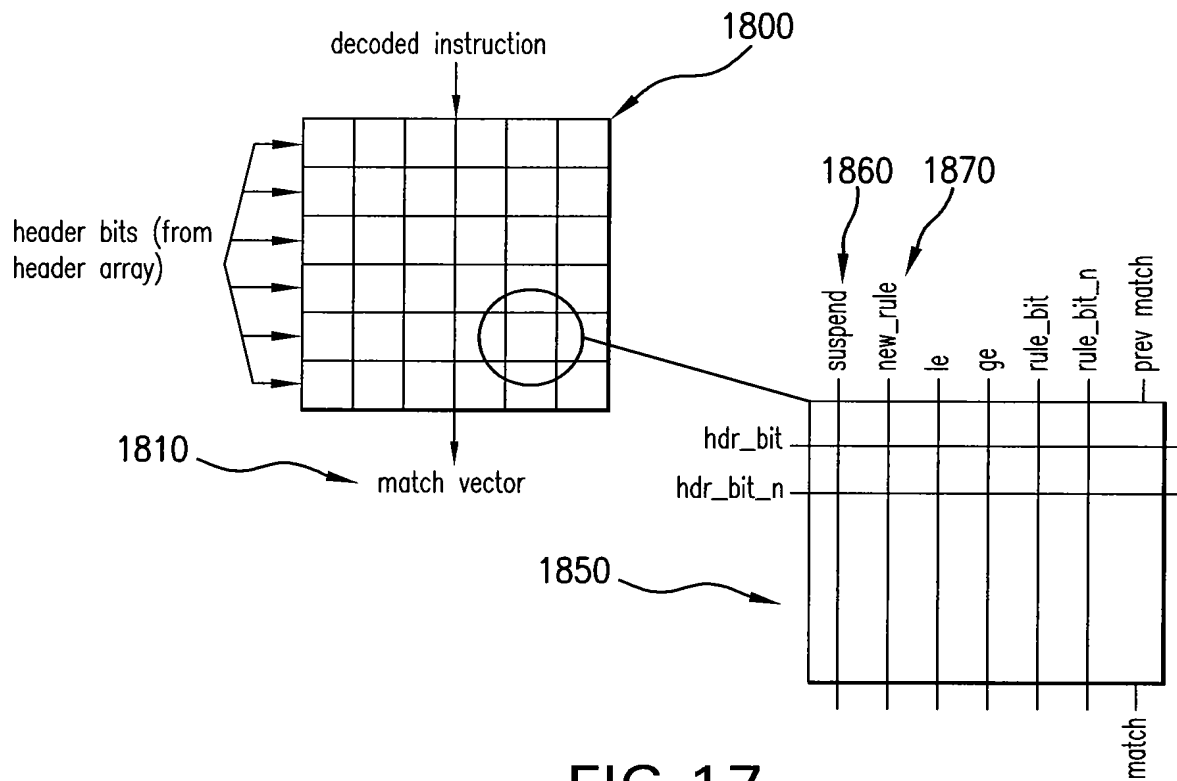


FIG. 17

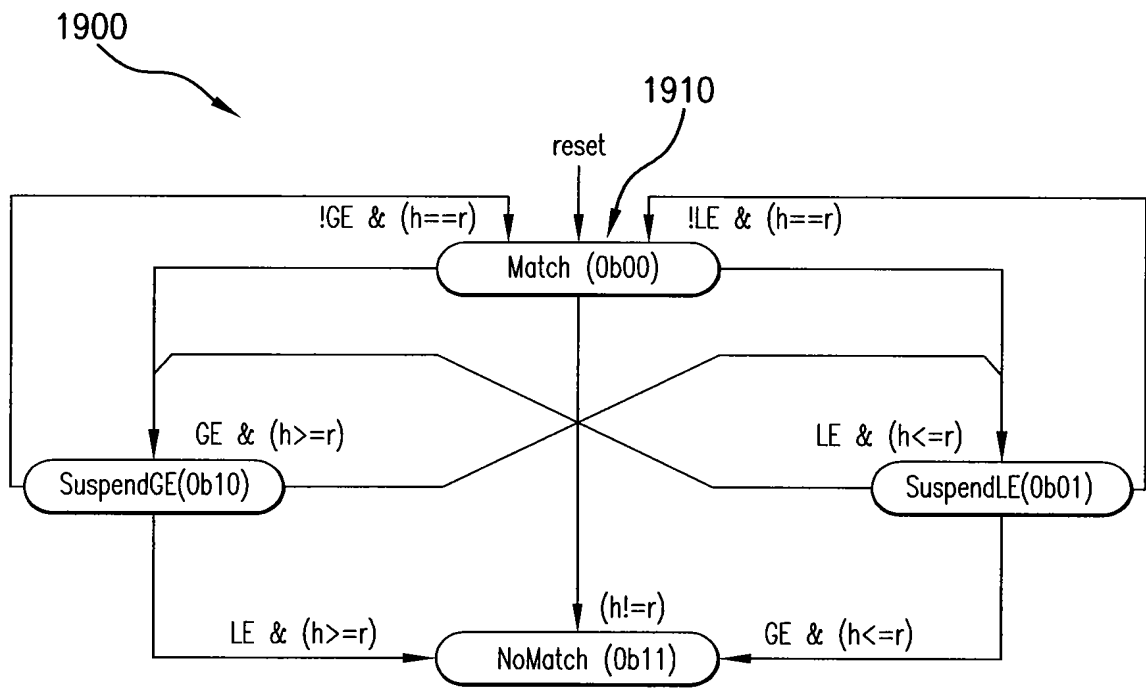


FIG. 18